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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: El-Kareh et al.

Art Unit: 2818

Serial No.: 10/822,078

Examiner: Vu, D.

Filed: 04/08/2004

Docket No.: TID-34937

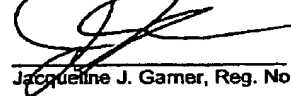
Customer No.: 23494

Conf. No.: 2204

For: METHOD OF FABRICATING COMPLEMENTARY BIPOLAR
TRANSISTORS WITH SiGe BASE REGIONSREQUEST FOR EXTENSION OF TIMECommissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on
the date shown below:

Jacqueline J. Garner, Reg. No. 36,144

November 15, 2005
Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner
for an extension of the shortened statutory period for response in the above-identified
Application.

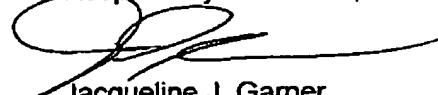
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Please charge the fee to deposit account no. 20-0668. Any further necessary
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265
(214) 532-9348
Fax: (972) 917-4418

Respectfully Submitted,

Jacqueline J. Garner
Reg. No. 36,144

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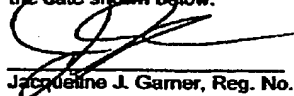
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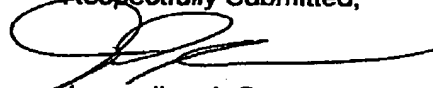
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